Exhibit P

U.S. Patent No. 6,356,984					
Claim No(s).	Claim Term	NetApp's Proposed Construction and Supporting Evidence	Sun's Proposed Construction and Supporting Evidence		
1, 14	"data bus"	 PROPOSED CONSTRUCTION: "an electrical pathway for transmitting data signals, which is distinct from a control bus" INTRINSIC EVIDENCE See 2:62-4:24; Figs. 1 and 2 and accompanying text. See Aug. 2, 2001 Preliminary Amendment at 7; Aug. 20, 2001 Notice of Allowability at 2. EXTRINSIC EVIDENCE See The Illustrated Dictionary of Electronics, 7th Ed. (1997): Data bus A conductor or medium over which digital data is transmitted from one place to another within a computer. One or more experts may testify concerning the meaning of this term to one of skill in the art in the context of its use in the '984 patent. NetApp reserves the right to rely on any evidence identified by Sun. 	PROPOSED CONSTRUCTION: "a Fibre Channel loop that carries data" EVIDENCE: • '984 patent: Col. 3: 2-12; Col. 3: 50-Col. 4:23; Col. 5:1-Col. 6: 28; and FIGs 1 and 2 and associated disclosures. • Testimony by Sun's expert, Dr. Levy, concerning the meaning of this term to one of skill in the art in the context of its use in the '984 patent. • Sun reserves the right to rely on any evidence identified by NetApp.		
1, 14	"control bus"	PROPOSED CONSTRUCTION: A bus separate from the data bus that carries control signals.	PROPOSED CONSTRUCTION: "a bus separate from the data bus that carries control signals"		

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Claim No(s).	Claim Term	NetApp's Proposed Construction and Supporting Evidence	Sun's Proposed Construction and Supporting Evidence		
		 INTRINSIC EVIDENCE See 2:62-4:24; Figs. 1 and 2 and accompanying text. See Aug. 2, 2001 Preliminary Amendment at 7; Aug. 20, 2001 Notice of Allowability at 2. EXTRINSIC EVIDENCE See The Illustrated Dictionary of Electronics, 7th Ed. (1997): Control bus In a digital computer, the electrical conductors linking the central-processing unit (CPU) control register to the memory circuits. See IBM Dictionary of Computing (1994): Control bus A bus carrying signals that regulate system operations. One or more experts may testify concerning the meaning of this term to one of skill in the art in the context of its use in the '984 patent. NetApp reserves the right to rely on any evidence identified by Sun. 	 EVIDENCE: ● '984 patent: Col. 3: 29-37; Col. 4:43-67; Col. 7: 40-53; FIG. 1 and FIG. 2 and associated disclosures. ● Testimony by Sun's expert, Dr. Levy, concerning the meaning of this term to one of skill in the art in the context of its use in the '984 patent. ● Sun reserves the right to rely on any evidence identified by NetApp. 		
1, 14	"control signals"	PROPOSED CONSTRUCTION: Electronic signals used to control internal or external devices or processes.	PROPOSED CONSTRUCTION: "signals controlling the state of storage subsystem devices."		
		<u>INTRINSIC EVIDENCE</u> • See 9:66-10:13; 10:55-13:35; Figs. 4 and 5	EVIDENCE: • '984 patent: Col. 4: 44 -67; Col. 11: 66 –		

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U.S. Patent No. 6,356,984					
Claim No(s).	Claim Term	NetApp's Proposed Construction and Supporting Evidence	Sun's Proposed Construction and Supporting Evidence		
		 device, a communications channel, a network station, a program, a bit, a byte, and so on. One or more experts may testify concerning the meaning of this term to one of skill in the art in the context of its use in the '984 patent. NetApp reserves the right to rely on any evidence identified by Sun. 			
1, 2, 9, 12, 14, 15, 22, and 25	"host"	 PROPOSED CONSTRUCTION: General purpose computer. INTRINSIC EVIDENCE See 1:43-56; 2:63-3:2; 3:13-19; Fig. 1 and accompanying text. EXTRINSIC EVIDENCE See The Illustrated Dictionary of Electronics, 7th Ed. (1997): Host A programmable computer that gathers and stores the information from all of the dataentry terminals in a system. See IBM Dictionary of Computing (1994): Host processor (1) A processor that controls all or part of a user application network (6) Synonymous with host computer. Host computer (2) The primary or controlling computer in a multiple computer installation. 	PROPOSED CONSTRUCTION: Sun contends this term does not require construction because the term is clear on its face. EVIDENCE: • '984 patent at Abstract, 1:6-9, 38-67; Col. 2:1-3, 61-62; Fig. 1 and associated disclosure. • Testimony by Sun's expert, Dr. Levy, concerning the meaning of this term to one of skill in the art in the context of its use in the '984 patent. • Sun reserves the right to rely on any evidence identified by NetApp.		

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U.S. Patent No. 6,356,984						
Claim No(s).	Claim Term	NetApp's Proposed Construction and Supporting Evidence	Sun's Proposed Construction and Supporting Evidence			
		 the '984 patent. NetApp reserves the right to rely on any evidence identified by Sun. 				
22	"attention command control signal"	PROPOSED CONSTRUCTION: A control signal that requests a status response. INTRINSIC EVIDENCE • See 9:66-10:13; 10:60-13:35; 22:52-56; Figs. 4 and 5 and accompanying text.	PROPOSED CONSTRUCTION: Sun contends this term does not require construction because the term is clear on its face. However, if the Court decides to construe this term, the term should be construed to mean: "a control signal transmitted to initiate a status			
		 EXTRINSIC EVIDENCE One or more experts may testify concerning the meaning of this term to one of skill in the art in the context of its use in the '984 patent. NetApp reserves the right to rely on any evidence identified by Sun. 	response." EVIDENCE: • '984 patent: Col. 11: 14-56. • Testimony by Sun's expert, Dr. Levy, concerning the meaning of this term to one of skill in the art in the context of its use in the '984 patent. • Sun reserves the right to rely on any evidence identified by NetApp.			

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